



$$\text{por_delay} = -R3 \cdot C8 \cdot \ln\left(1 - \frac{5 \cdot 10}{17.5}\right) / 3.3$$

$$= 10 \text{ ms}$$

- 1 R6 and R7 provide the 5 mA minimum load current that the TLV1117 requires in order to maintain regulation. They may or may not be required depending on the application's minimum load current specification.
- 2 Additional input may be required depending on the regulator's proximity to the system power supply. Additional output capacitance will be required to meet load transient requirements.
- 3 U4 has an open collector output and so will need a pull up resistor (R8 in this design) if not connected to a pin that is internally pulled up.

TEXAS INSTRUMENTS

Title TLV1117 based F28xxx DSP Power		
Size B	Number PR672	Rev E-1
Date 10/8/07	Drawn by	
Filename pr672e-1.sch	Sheet of	

Filename: PR672E-1_bom.xls						
Date: 10/15/2007						
PR672E-1 BOM						
COUNT	RefDes	Value	Description	Size	Part Number	MFR
2	C1, C2	10uF	Capacitor, Ceramic, 6.3V, X5R, 20%	0603	STD	STD
3	C3, C4, C8	0.1uF	Capacitor, Ceramic, 6.3V, X5R, 20%	0603	STD	STD
2	C5, C6	100uF	Capacitor, Aluminum, 6.3V, ±20%	0.201 x 0.262 in	EEVFK0J101P	Panasonic
1	C7	1.0uF	Capacitor, Ceramic, 6.3V, X5R, 20%	0603	STD	STD
1	C9	0.1uF	Capacitor, Ceramic, 6.3V, X5R, 20%	0603	STD	STD
2	D1, D2	1N4002	Diode, Signal, 100V, 1A	DO-41	1N4002	Diodes
1	R1	23.7k	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	R2	56.2k	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	R3	49.9k	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	R4	7.5k	Resistor, Chip, 1/16W, 1%	0603	STD	STD
2	R5, R8	10k	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	R6	357	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	R7	660	Resistor, Chip, 1/16W, 1%	0603	STD	STD
1	U1	TLV1117-18IKVU	IC, 1.8V, 800mA LDO Voltage Regulator	Power-Flex	TLV1117-18IKVU	TI
1	U2	TLV1117-33IKVU	IC, 3.3V, 800mA LDO Voltage Regulators	Power-Flex	TLV1117-33IKVU	TI
1	U3	TPS3805H33DCK	IC, Voltage Detector,	SOP-5 (DCK)	TPS3805H33DCK	TI
1	U4	TL331DBV	IC, COMPARATOR, DIFFERENTIAL, SINGLE	SOT_23_5 (DB)	TL331DBV	TI
Notes:						
1. These assemblies are ESD sensitive, ESD precautions shall be observed.						
2. These assemblies must be clean and free from flux and all contaminants.						
Use of no clean flux is not acceptable.						
3. These assemblies must comply with workmanship standards IPC-A-610 Class 2.						
4. Ref designators marked with an asterisk (***) cannot be substituted.						
All other components can be substituted with equivalent MFG's components.						

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